

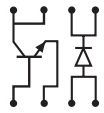


## Multi Discrete Module<sup>TM</sup> in a Tiny Leadless Module<sup>TM</sup>

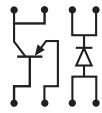
**1Amp, 40V Low  $V_{CE(SAT)}$  Transistor and  
1Amp, 40V Low  $V_F$  Schottky Rectifier**



**TLM832D**  
8 PIN  
3x2mm



**NPN**  
CTLM1034-M832D



**PNP**  
CTLM1074-M832D



### features

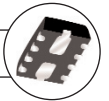
- Two Isolated Discrete Devices
- High Current 1.0A Small Signal Transistor and 1.0A Schottky Rectifier.
- **CTLM1034-M832D:**  
Low  $V_{CE(SAT)}$  NPN Transistor (450mV @  $I_C = 1.0A$  Max) and Low  $V_F$  Schottky Rectifier (550mV @ 1.0A Max)
- **CTLM1074-M832D:**  
Low  $V_{CE(SAT)}$  PNP Transistor (450mV @  $I_C = 1.0A$  Max) and Low  $V_F$  Schottky Rectifier (550mV @ 1.0A Max)
- High Power to Footprint Ratio of 275mW per sq mm (Package Power Dissipation / Package Surface Area)
- Small TLM 3x2mm Leadless Surface Mount Package

### description

The Central Semiconductor Multi Discrete Module CTLM1034-M832D (NPN) and CTLM1074-M832D (PNP) consists of a Low  $V_{CE(SAT)}$  NPN or PNP Transistor and a Low  $V_F$  Schottky Rectifier. Packaged in a small, thermally efficient, 3x2mm Tiny Leadless Module, these devices are designed for applications where small size, operational efficiency, and low energy consumption are the prime requirements. Due to its leadless package design, this device is capable of dissipating up to 4 times the power of similar devices in comparable sized surface mount packages.

Sample Devices  
available  
upon request.

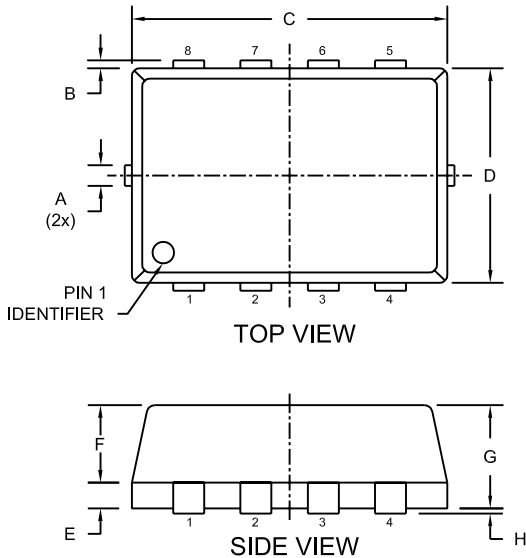
### selection guide



TYPE NO.	DESCRIPTION	Low $V_{CE(SAT)}$ Small Signal Transistor Specifications						Low $V_F$ Schottky Diode Specifications				
		$BV_{CBO}$ (V) MIN	$BV_{CEO}$ (V) MIN	$BV_{EBO}$ (V) MIN	$V_{CE(SAT)}$ (V)		@ $I_C$ (mA)	$V_{RRM}$ (V) MAX	$I_F$ (A) MAX	$V_F$ (V) MAX	@ $I_F$ (mA)	
CTLM1034-M832D	Combination: Low $V_{CE(SAT)}$ NPN transistor and Low $V_F$ Schottky Rectifier	40	25	6.0	0.13	0.25	500	+	40	1.0	0.45	500
CTLM1074-M832D	Combination: Low $V_{CE(SAT)}$ PNP transistor and Low $V_F$ Schottky Rectifier	40	25	6.0	0.15	0.25	500	+	40	1.0	0.45	500

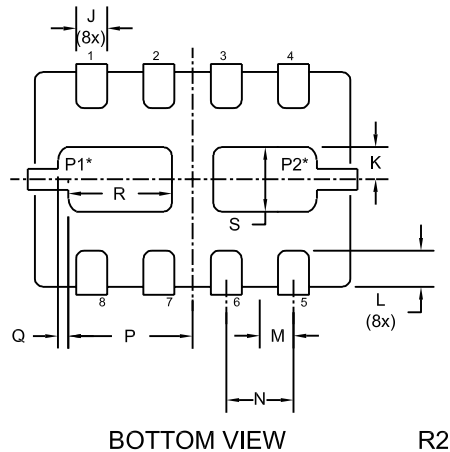
# Multi Discrete Module™ in a Tiny Leadless Module™

## Mechanical Specifications-TLM832D



SYMBOL	INCHES		MILLIMETERS	
	MIN	MAX	MIN	MAX
A	0.007	0.012	0.170	0.300
B	-	0.005	-	0.125
C	0.114	0.122	2.900	3.100
D	0.075	0.083	1.900	2.100
E	0.006	0.010	0.150	0.250
F	0.026	0.030	0.650	0.750
G	0.031	0.039	0.800	1.000
H	0.000	0.002	0.000	0.050
J	0.009	0.013	0.240	0.340
K	0.006	0.014	0.160	0.360
L	0.008	0.018	0.200	0.450
M	0.013		0.325	
N	0.026		0.650	
P	0.040	0.048	1.010	1.210
Q	0.004		0.100	
R	0.032	0.040	0.820	1.020
S	0.017	0.025	0.430	0.630

TLM832D (REV: R2)



BOTTOM VIEW R2

\* Note:

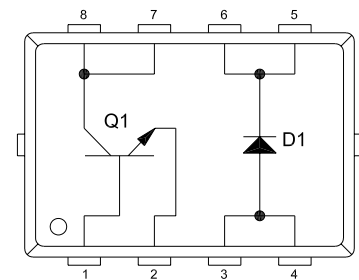
- Exposed pad P1 common to pins 7 and 8
- Exposed pad P2 common to pins 5 and 6

### CTLTM1034-M832D

#### LEAD CODE:

- 1) BASE Q1
- 2) EMITTER Q1
- 3) ANODE D1
- 4) ANODE D1
- 5) CATHODE D1
- 6) CATHODE D1
- 7) COLLECTOR Q1
- 8) COLLECTOR Q1

MARKING CODE: CFD

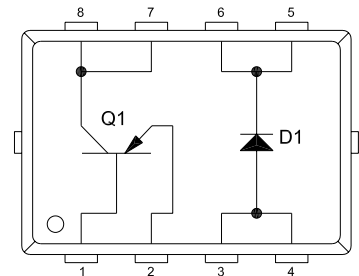


### CTLTM1074-M832D

#### LEAD CODE:

- 1) BASE Q1
- 2) EMITTER Q1
- 3) ANODE D1
- 4) ANODE D1
- 5) CATHODE D1
- 6) CATHODE D1
- 7) COLLECTOR Q1
- 8) COLLECTOR Q1

MARKING CODE: CFC



**Central**™  
Semiconductor Corp.

Innovations in Discrete  
Semiconductors

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PA CTLM1034-M832D